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PATENT ABSTRACTS OF JAPAN

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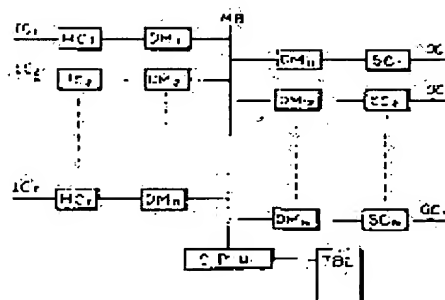
(72)Inventor : FUJIOKA MASANORI
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(54) SEMI-STORING TYPE PACKET SWITCHING SYSTEM

(57)Abstract:

PURPOSE: To accurately transfer a packet to an opponent by using an internationally standardized packet format, by performing error check at the time of receiving a header part, deleting the packet in receiving if an error exists, and deciding an output path and an output line according to the header part if no error exists.

CONSTITUTION: When the packet transmitted on an input line IC1 is transferred to the output line OC2, a part up to the header part is received at a header analysis circuit, and when no transfer error exists, the content of the said header part is written on a dual port memory DM1. A central processing unit CPU detects the beginning of the write of the packet on the DM, and rewrites the header part to a value to be used on the output line OC2 according to the content of a table TBL. In a case where the output line OC2 is in use when the transmission of the packet is desired to start, the content of the packet in receiving is written on the DM1 in order, and the transfer of the packet from the DM1 to a DM12 and the sending of the packet are started at a time when the OC2 becomes vacant.



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